Total No. of Printed Pages:2

SUBJECT CODE NO:- H-135
FACULTY OF SCIENCE AND TECHNOLOGY
S.E. (EC/ECT/IEC/E&C) (Sem-II)
High Speed Analog Devices
[REV]

[Time: Three Hours] **[Max.Marks:80]**

Please check whether you have got the right question paper.

N.B
1. Q.No.1 and 6 are compulsory.
2. Solve any two questions from remaining from section A and B.

Section A

Q.1 Solve any five. 10
1) Enlist Non-linear Applications of OP-Amp.
2) Draw pin diagram of LM339.
3) Draw pin diagram of 565.
4) Define Lock Range.
5) What is slew Rate?
6) Define CMRR.

Q.2 a) Explain Summing Amplifier. 07
b) Explain peak detector. 08

Q.3 a) Explain operating principle of phase lock loop. 07
b) Explain voltage to frequency convertor. 08

Q.4 a) Explain AD8001 with its features and Applications. 07
b) Compare between AD8001 and AD8002. 08

Q.5 a) Explain Precision Rectifier. 07
b) Explain ideal characteristics of OP-Amp. 08

Section – B

Q.6 Solve any five 10
1) Enlist different features of High speed ADC.
2) What do you mean by sampling
3) State successive approximation principle.
4) Explain Benefits of HEMT.
5) Enlist Applications of AD8011
6) Explain features of AD847

Q.7	a)	Explain the Applications of AD8001.	07
	b)	Explain Noise comparison between VFB & CFB Amplifier.	08
Q.8	a)	Explain working principle of HEMT	07
	b)	Write notes on Material used for high speed devices.	08
Q.9	a)	Explain differential live driver.	07
	b)	Explain Mixer.	08
Q.10	a)	Explain software Radio.	07
	b)	Explain successive approximation ADC.	08